

Appln No. 10/674,693

Amdt date January 23, 2006

Reply to Office action of September 22, 2005

**REMARKS/ARGUMENTS**

In the Office action dated September 22, 2005, claims 1, 2, 4 - 15 and 17 - 27 were rejected under 35 U.S.C. § 103. Claims 3 and 16 were objected to as being dependent upon a rejected base claim, but were deemed allowable if rewritten in independent form including all limitations of the base claim and any intervening claims.

By this Amendment, Applicant has amended claims 1, 2, 4 - 8 and 15, added claims 28 - 41 and canceled claims 3, 9 - 13, 16 and 22 - 27. Reconsideration and reexamination are hereby requested for claims 1, 2, 4 - 8, 14, 15, 17 - 21 and 28 - 41 that are now pending in this application.

**Response to the 35 U.S.C. § 103 Rejection of the Claims**

Claims 1, 2, 4 - 15 and 17 - 27 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Reiger, III, U.S. Patent No. 5,732,324 (hereafter referred to as "Reiger"), in view of Banu et al., U.S. Patent No. 5,757,872 (hereafter referred to as "Banu"). Claims 9 - 13 and 22 - 27 have been canceled. Claims 1 and 14 are independent.

Reiger discloses an antenna 1, an FM demodulator, 2, a sync detector and clock generator 3, a FIFO 5 and a microprocessor 6. Reiger makes no mention as to how "the microprocessor reads the incoming data signal from the FIFO."

In contrast, claim 1 recites, in part: "a receiver circuit that receives a wireless signal; a demodulator coupled to the receiver circuit, the demodulator recovering a data signal and at least one clock signal from at least one signal output by the

**Appln No. 10/674,693**

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receiver circuit; a computer configured to generate a read signal; and a first-in first-out memory coupled to the demodulator to receive the data signal and the at least one clock signal, wherein the first-in first-out memory stores the data signal in response to the at least one clock signal, and wherein the first-in first-out memory is coupled to the computer to receive the read signal" Accordingly, Reiger does not teach or suggest the specific limitations of claim 1.

Similarly, claim 14 recites, in part: "receiving a wireless signal; recovering a data signal and at least one clock signal from the received wireless signal; storing the data signal into a first-in first-out memory in response to the at least one clock signal; and providing, by a computer, a read signal to the first-in first-out memory." Reiger thus does not teach or suggest the specific limitations of claim 14.

In addition, in an effort to expedite prosecution of the allowed claims, Applicant has amended claims 1 and 14 to incorporate the limitations of claims 3 and 16, respectively. Applicant submits this also makes the double patenting rejection moot. In view of the above, Applicant submits that claims 1 and 14 are not obvious in view of Reiger and Banu.

Claims 2, 4 - 8, 15, 17 - 21 that depend on either claim 1 or claim 14 also are patentable over the cited references for the reasons set forth above. In addition, these dependent claims are patentable over the cited references for the additional limitations that these claims contain.

**Appln No. 10/674,693**

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New claims 28 - 41

Applicant has added new claims 28 - 41. Claims 28 and 35 are independent. Claim 28 recites, in part: "a processing circuit coupled to the first-in first-out memory to retrieve the stored data signal in accordance with a read signal that is not synchronized to a clock signal derived from the transmit clock signal." Claim 35 recites, in part: "storing the data signal into a first-in first-out memory; and retrieving the stored data signal in accordance with a read signal that is not synchronized to a clock signal derived from the transmit clock signal." Applicant submits that the cited references do not teach or suggest all of the limitations of these claims.

Claims 29 - 43 and 36 - 41 that depend on either claim 28 or claim 35 also are patentable over the cited references for the reasons set forth above. In addition, these dependent claims are patentable over the cited references for the additional limitations that these claims contain.

Appln No. 10/674,693

Amdt date January 23, 2006

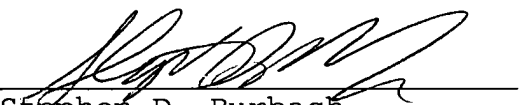
Reply to Office action of September 22, 2005

**CONCLUSION**

In view of the above remarks Applicant submits that the claims are patentably distinct over the cited references and that all the objections/rejections to the claims have been overcome. Reconsideration and reexamination of the above application is requested.

Respectfully submitted,  
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